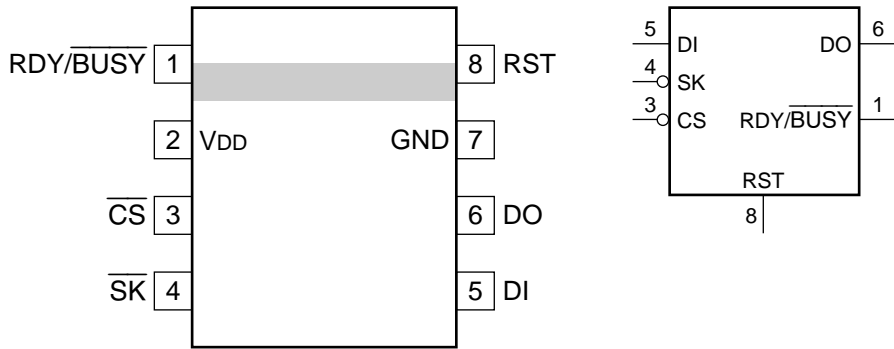


C-MOS 8 K (512 × 16)-BIT SERIAL EEPROM

—TOP VIEW—



- $\overline{\text{CS}}$: CHIP SELECT INPUT
 DI : SERIAL DATA INPUT
 DO : SERIAL DATA OUTPUT
 $\text{RDY}/\overline{\text{BUSY}}$: READY/ $\overline{\text{BUSY}}$ OUTPUT
 RST : RESET INPUT
 $\overline{\text{SK}}$: SERIAL DATA CLOCK INPUT

